Homework 1

1. Write a survey of digital IC design CAD tools from major EDA companies.

2. What is VHDL and give an example. Explain where a designer uses VHDL?

3. Write a VHDL code of a 2-bit adder in both structure and behavior manner.

* This homework is due at September 7, 2012.

* Only submit your homework in the electronic version to TA