Application Specific Integrated Circuit Design
August 31, 2012

Topics

This course discusses the design of Application Specific Integrated Circuits (ASICs). Specific topics include: VLSI design flow, custom and semi-custom IC structures, synthesis and CAD tools, system specification development, and the implementation of a digital signal processing (DSP) chip.

Prerequisite

This course is designed for the first or second year graduate students. Attendents should have taken the undergraduate senior or graduate lever courses in the area of VLSI design, VHDL and C language, and have the basic knowledge of VLSI CAD tools.

Text book

Reference


**Policy**

- Attendance is required.
- Homework and project reports need to be handed in before the due time.
- There will be a middle term and a final project.
- There will be three reading and writing assignments.
- To see the instructor other than the class meeting time, please make appointment.
- **Grading**
  - Distribution: attendance 2%, presentation 3%, homework 20%, middle term project 25%, and final project 50%.
Instructor information

- Name: D. Zhou
- Office: ECN 4. 610
- Email: zhoud@utdallas.edu
- Phone: (972) 883 4392

Instructor office hour

- F 2:00pm-3:00pm
- Make appointment if you need to see the instructor or TA other than the office hour

TA office hour

- Name: Minghua Li
- Email: mxl095420@utdallas.edu
- Office: ECSN 3.516
- Office hour: Thursday 10-11am (or by appointment)
- Website: www.utdallas.edu/~mxl095420 (All course related materials will be posted on the webpage and please check it regularly.)