

**University of Texas at Dallas**  
**EE 3311**  
**Fall 2005**

**Instructor**

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Class Meetings: Tuesdays and Thursdays, ECSS 2.203 8:00 to 9:15 A.M.

Office hours: After class or by appointment.

Web page: TBA

TA: TBA

**Catalog Description**

EE 3311 Electronic Circuits (3 semester hours) Analysis and design of electronic circuits using diodes, transistors and operational amplifiers with feedback. Gain and stability of basic amplifier circuits using BJTs, JFETs and MOSFETs; classes of amplifiers; performance of ideal and non-ideal operational amplifiers. Prerequisites: **EE/TE 3301**, EE 3310. Co-requisite: EE 3111. (3-0) S

**Textbooks:**

- 1 Microelectronic Circuits, by Sedra and Smith, 5<sup>th</sup> Edition, Oxford University Press.
- 2 Any SPICE book ( not required)

**Software:**

Access to a circuit simulator such as PSpice, Microcap, or Analog Workbench is required.

**Grading**

- 1 Exams: 80% (2 exams 25% each + Final 30%) . Closed book, closed notes.
- 2 Homework: 20% Each homework is due a week after it is assigned.
- 3 Quizzes: Each quiz will be counted as one homework.
- 4 Mini spice projects: each project is counted as one homework ( Projects can not be omitted from "HW lowest grade")
- 5 Two lowest grade of all HW will be dropped

**Exams:**

Exam1: Tuesday Sept. 27

Exam2: Tuesday Nov. 1

Final: To be announced by University

**Notes:**

1. **Homework Policy:** Homework is due at the beginning of class on the designated date.
2. Be prepared for pop-quizzes: have your calculator handy!
3. It will be assumed that the student has good knowledge and can solve any of the circuit problems from the ENA course.
4. Homework and exams must be done individually . A team of no more than two students will be allowed for SPICE projects.
5. Show all your work (including all steps) in the solution of homework, exams, and quizzes.
6. **Scholastic Dishonesty:** The value of an academic degree depends on the absolute integrity of the work done by the student to earn that degree. It is imperative that students maintain a high level of individual honor in his or her scholastic work. Scholastic dishonesty will not be tolerated.
7. Try to arrive in class on time. If you arrive late, you are disrupting the learning environment for everyone. If you arrive late on the day of a quiz or exam, and the first person to finish has already left the classroom, you will not be permitted to begin the quiz or exam.