

EE 2310 Course Syllabus – University of Texas at Dallas

Course Information: EE 2310 – Introduction to Digital Systems; Spring, 2012
Class Schedules: Section 002 – 11:30 AM-12:45 PM, TR, ECSS 2.415
Section 501 – 7:00-8:15 PM, TR, ECSS 2.415

Instructor: Dr. Nathan Dodge Tel: (972) 883-2951 email: dodge@utdallas.edu
Office: ECSN 4.916 **Office Hours:** TR, 4:00-6:00 PM and by appointment
Instructors Website: <http://www.utdallas.edu/~dodge/>

NOTE: I DO NOT USE WEBCT OR eLEARNING. TO GET INFORMATION ABOUT EE 2310 YOU MUST GO TO THE WEBSITE LISTED ABOVE.

Course Pre-requisite: None. The only required prior knowledge or skill is a working ability in basic algebra and knowledge of some of the fundamentals of programming.

Course Description: This course covers principles of digital systems, assembly language programming, and an overview of computer architecture. It provides a background in basic technology areas that are required to understand computer architecture and design.

Student Learning Objectives/Outcomes: By the end of EE 2310, the student should have a firm basic knowledge of:

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| a. Binary and hexadecimal numbers | d. Sequential logic circuits |
| b. Fundamentals of Boolean Algebra and digital logic gates | e. Assembly language programming |
| c. Combinational logic circuits | f. Fundamentals of computer architecture |
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Required Textbooks and Materials:

- **Computer Organization and Design**, Fourth Edition, David Patterson and John Hennessy (Morgan Kaufmann, 2009).
 - **Schaum's Outline of Theory and Problems of Digital Principles**, Third Edition, Roger L. Tokheim, (McGraw-Hill).
 - **A Programmer's Guide to Assembler**, Third Edition, William Pervin, (McGraw-Hill, 2010). **Note:** Some second edition copies may be available, and they are acceptable.
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Suggested Course Materials:

- **LogicWorks 5**, Capilano Computing Systems, Inc. (Addison-Wesley, 2009).

There are no other required or reference materials. All homework and practice exercises are available on-line at the instructor's website. In addition, review material ("test reviews") are also available on-line and will be worked in class during the test review period just prior to each semester test.

EE 2310 Session Schedule, Spring, 2012, Sections 001 and 501

Week of (Monday)	Session #	Lecture #	Text Chapter/Sect. References	Class Date	Topic
<u>1/16</u>	1	1	PH 1 (for interest)	Jan. 17	Class overview, history of computing.
	2	2	PH 2.4; T 1-2	Jan. 19	Binary numbers, hexadecimal numbers, numeric conversions.
<u>1/23</u>	3	3	T 3-4; PH C.1-C.3*	Jan. 24	Signed binary numbers and binary codes.
	4	4	T 4, 8; PH C.5	Jan. 26	Boolean algebra and combinational Logic.
<u>1/30</u>	5	5	T 5	Jan. 31	Karnaugh maps; logic simplification.
	6	6	T 9; PH C.7-C.8	Feb. 2	Complex combinational logic. Lab 1 brief.
<u>2/6</u>	7	7	T 9	Feb. 7	Flip-Flops, the foundation of sequential logic. Lab 2 brief.
	8	8	T 9, 10	Feb. 9	Registers, counters, latch-based circuits.
<u>2/13</u>	9	---	---	Feb. 14	Test #1 review. TR #1 due, start of class.
	10	---	---	Feb. 16	Test #1.
<u>2/20</u>	11	9	T 10, 11	Feb. 21	Test #1 recap. Designing sequential logic. Lab 3 brief.
	12	10	P 3; PH 2.5-2.10**	Feb. 23	Introduction to SPIM, registers and register-register (arithmetic/logical) instructions. Semester design project.
<u>2/27</u>	13	11	P 2-2.2, App. B & D, 6.4	Feb. 28	MIPS instruction set, directives, and system calls. Lab 4 brief.
	14	12	P 3, class notes	Mar. 1	Writing and executing MIPS programs.
<u>3/5</u>	15	13	P 2.3, 4	Mar. 6	Memory reference instructions.
	16	14	P 1.6	Mar. 8	MIPS shift and logical instructions; branch instructions.
<u>3/12-3/16</u>	---	---	---	---	Spring Break!!!!
<u>3/19</u>	17	15	P 4-4.3	Mar. 20	Loops in SPIM. Lab 5 brief.
	18	16	Class notes	Mar. 22	SPIM programming example.
<u>3/26</u>	19		Class notes	Mar. 27	Test #2 review. TR #2 due, start of class.
	20	---	---	Mar. 29	Test #2.
<u>4/2</u>	21	17	P 5, 6, 7	Apr. 3	Test #2 recap. Procedures (recursion); use of the stack.
	22	18	P 12-12.3, App. C	Apr. 5	Computer operation, processes, final programming lesson. Lab 6 brief.
<u>4/9</u>	23	---	---	Apr. 10	Class programming exercises.
	24	19	PH 4.1-4.3	Apr. 12	Computer architecture: ALU design.
<u>4/16</u>	25	20	PH 4.4	Apr. 17	ALU review; design of CPU control unit. Multicycle implementation.
	26	21	P 11-11.2, PH 4.5-4.8	Apr. 19	Pipelined processors.
<u>4/23</u>	27	22	P 11.3, PH 5, 6***	Apr. 24	Modern memory architecture.
	28	---	Class notes	Apr. 26	Test #3 review. TR #3 due, start of class.
<u>4/30</u>	29	---	---	May 1	Reserved period.
	30	---	---	May 3	Test #3.
<u>5/7</u>	---	---	---	---	Classes end; no final exam

Abbreviations: P – Pervin, A Programmer’s Guide to Assembler; T – Tokheim, Schaum’s Outline of Theory and Problems of Digital Principles; PH – Patterson and Hennessy, Computer Organization and Design

* Appendices C, D, and E are on the CD-ROM which comes with the PH textbook.

** PH Chapter 2 is an excellent programming reference.

*** Optional; this is dense material.

Grading Policy: Grading will be on the following basis:

Homework	--	10 %
Test Review Sheets	--	1 Bonus Point each
Lab Grades	--	15 %
Semester Design Project	--	5 %
Semester examinations		
	#1	-- 20 %
	#2	-- 25 %
	#3	-- <u>25 %</u>
Total	--	<u>103 %</u>

Grade ranges for EE 2310 are: A+: 97-100; A: 93-96; A-: 90-92; B+: 87-89; B: 83-86; B-: 80-82; C+: 77-79; C: 73-76; C-: 70-72; D+: 67-69; D: 63-66; D-: 60-62; F: below 60.

Course & Instructor Policies: Course notes, homework, and a copy of this syllabus are available on-line at the instructor's faculty home page (address shown above). Course note files are labeled by lecture. All on-line material is in Adobe Acrobat® (.pdf) format. You will need Adobe Acrobat Reader to read pdf files. It may be downloaded from the UTD software web site (whose web address is: <http://www.utdallas.edu/ir/local/index.html>), or from the Adobe website (<http://www.adobe.com/products/acrobat/readstep2.html>).

Exams: Three exams will be given during the semester. **No make-up exams will be given, except under dire circumstances, such as (serious) illness.** Although 2310 sections 001 and 501 are taught on identical schedules, there will be no switching of class periods for test-taking purposes. **You must take each test at the time of the class session that you attend.** You may, however, attend the other class lecture if you have a conflict on a particular day with your usual lecture period.

Homework: Homework is due on the date shown on the homework schedule. Homework answers will be posted on-line after the due date. Homework is worth 15% of your final grade (counting the semester design project). "Test review sheets" will be worked in class during the test review class period. Test reviews are posted on-line and are basically a second type of homework. These are due at the beginning of the review class period. Test review sheet answers are NOT posted on-line. The three reviews constitute a small bonus to your final grade. The test reviews must be shown to the instructor at the beginning of class, to receive a "check." The three test reviews are worth 3 points on your final grade, 1 point each. Selected in-class exercise sheets may be turned in, for a +1-3 bonus on the next test. A few select class problems may not be worked in class, but assigned as bonus problems. Correctly completed, they are worth 1-3 bonus points on the test after the problem is turned in.

Labs: There are four digital logic labs in EE 2310 which must be done in the lab room (ECSS 4.622). The labs are listed on the EE 2310 web site (labs 1-4). You will work with a lab partner, and each lab team will submit a single lab report. You and your lab partner will do labs on your own, write the lab reports, and turn them in on the due date shown. Late lab reports are not accepted. Partners must take turns writing the lab report. The initial EE 2310 class period will include a lab introduction. There are also two software assignments that count as labs, but no report is required for these two exercises. The team will simply email the completed program to the TA for grading. Labs count 15 % of your final grade. Make sure the names of BOTH partners are prominently listed on the software lab input, so that homework grades may be properly credited. See also note below on software submissions.

Notes on Semester Project:

1. The semester project is generally a digital logic design project. (Occasionally in the past, a software project has been given, as suits the instructor.)
2. Digital circuit designs must be done using a computer layout tool. LogicWorks™ (see list of texts above) can do this very well.

3. Although you do not have to purchase LogicWorks, #2 still applies. You may use another tool (such as Pspice) if you have access to it. Some students have even used Word Draw, although this can be very tedious. If you choose not to purchase LogicWorks, some free layout software tools are listed on the 2310 website.
4. You may work with your lab partner on the design and construction of your project. However, each partner submits a separate homework. **Please include your lab partner's name on your project submission.**
5. **Late semester projects are not accepted.**

Submission Mechanics for Homework and Lab Reports: Due dates for homework and lab submissions can be found in the “Lab Report Due Dates” and “Homework Due Dates” schedules on the EE 2310 web site. Most required class work (including lab reports on labs 1-4) will be brought to class and submitted to the TA’s, who will collect material at the beginning of class. Some material, specifically homework #7 and the two software labs, will be emailed directly to the TA on the due date specified in the “Lab Report Due Dates” and “Homework Due Dates” schedules.

Special notes:

1. In the case of labs 5 and 6, only one program is required per team. **MAKE SURE YOU LIST BOTH PARTNER’S NAME ON THE PROGRAM OR PROPER CREDIT MAY NOT BE GIVEN!**
2. Please note the different requirements for homework and labs: Each student submits all homework assignments (including test reviews and the semester project) **independently!** Labs (including the two software labs) require **only one submission by the team.**
3. That is, **each student submits every homework, test review, and the semester project independently.** However, lab partners or other student groups may work (and indeed are encouraged to work) on homework together; such “homework sessions” with multiple students often amplifies the learning process.

Class attendance: Class attendance is not often taken. You are expected to attend class to be sure that you can master all the (considerable) course material by the end of the semester. Although points are not usually deducted for lack of attendance, it **can** be done in particularly abusive circumstances. **More importantly, if you do not attend EE 2310 classes regularly, you will fail.**

Classroom citizenship, etc.): Proper classroom deportment for engineering students is taken for granted. When you enter the classroom, speak in hushed tones if you are carrying on a conversation. **MAKE SURE YOU TURN OFF YOUR CELL PHONE.** Do not listen to mp3 players, Ipods, etc., in class. Students found playing computer games in class will be criticized, ridiculed, and possible have points taken off their next test grade! **Note: Keep your laptop turned off unless advised otherwise by the instructor.** Students are not allowed to have laptops operating in general. On occasion, students will be allowed to use their laptops during certain classroom exercises.

Field Trip Policies and Off-campus Instruction and Course Activities: No off-campus activities in this course.

Student Conduct & Discipline: The University of Texas System and The University of Texas at Dallas have rules and regulations for the orderly and efficient conduct of their business. It is the responsibility of each student and each student organization to be knowledgeable about the rules and regulations which govern student conduct and activities. General information on student conduct and discipline is contained in the UTD publication, *A to Z Guide*, which is provided to all registered students each academic year.

The University of Texas at Dallas administers student discipline within the procedures of recognized and established due process. Procedures are defined and described in the *Rules and Regulations, Board of Regents, The University of Texas System, Part 1, Chapter VI, Section 3*, and in Title V, Rules on Student Services and Activities

of the university's *Handbook of Operating Procedures*. Copies of these rules and regulations are available to students in the Office of the Dean of Students, where staff members are available to assist students in interpreting the rules and regulations (SU 1.602, 972/883-6391).

A student at the university neither loses the rights nor escapes the responsibilities of citizenship. He or she is expected to obey federal, state, and local laws as well as the Regents' Rules, university regulations, and administrative rules. Students are subject to discipline for violating the standards of conduct whether such conduct takes place on or off campus, or whether civil or criminal penalties are also imposed for such conduct.

Academic Integrity: The faculty expects from its students a high level of responsibility and academic honesty. Because the value of an academic degree depends upon the absolute integrity of the work done by the student for that degree, it is imperative that a student demonstrate a high standard of individual honor in his or her scholastic work.

Scholastic dishonesty includes, but is not limited to, statements, acts or omissions related to applications for enrollment or the award of a degree, and/or the submission as one's own work or material that is not one's own. As a general rule, scholastic dishonesty involves one of the following acts: cheating, plagiarism, collusion and/or falsifying academic records. Students suspected of academic dishonesty are subject to disciplinary proceedings.

Plagiarism, especially from the web, from portions of papers for other classes, and from any other source is unacceptable and will be dealt with under the university's policy on plagiarism (see general catalog for details). This course will use the resources of turnitin.com, which searches the web for possible plagiarism and is over 90% effective.

Email Use: The University of Texas at Dallas recognizes the value and efficiency of communication between faculty/staff and students through electronic mail. At the same time, email raises some issues concerning security and the identity of each individual in an email exchange. The university encourages all official student email correspondence be sent only to a student's U.T. Dallas email address and that faculty and staff consider email from students official only if it originates from a UTD student account. This allows the university to maintain a high degree of confidence in the identity of all individual corresponding and the security of the transmitted information. UTD furnishes each student with a free email account that is to be used in all communication with university personnel. The Department of Information Resources at U.T. Dallas provides a method for students to have their U.T. Dallas mail forwarded to other accounts.

Withdrawal from Class: The administration of this institution has set deadlines for withdrawal of any college-level courses. These dates and times are published in that semester's course catalog. Administration procedures must be followed. It is the student's responsibility to handle withdrawal requirements from any class. In other words, I cannot drop or withdraw any student. You must do the proper paperwork to ensure that you will not receive a final grade of "F" in a course if you choose not to attend the class once you are enrolled.

Student Grievance Procedures: Procedures for student grievances are found in Title V, Rules on Student Services and Activities, of the university's *Handbook of Operating Procedures*.

In attempting to resolve any student grievance regarding grades, evaluations, or other fulfillments of academic responsibility, it is the obligation of the student first to make a serious effort to resolve the matter with the instructor, supervisor, administrator, or committee with whom the grievance originates (hereafter called "the respondent"). Individual faculty members retain primary responsibility for assigning grades and evaluations. If the matter cannot be resolved at that level, the grievance must be submitted in writing to the respondent with a copy of the respondent's School Dean. If the matter is not resolved by the written response provided by the respondent, the student may submit a written appeal to the School Dean. If the grievance is not resolved by the School Dean's decision, the student may make a written appeal to the Dean of Graduate or Undergraduate Education, and the dean will appoint and convene an Academic Appeals Panel. The decision of the Academic Appeals Panel is final. The results of the academic appeals process will be distributed to all involved parties.

Copies of these rules and regulations are available to students in the Office of the Dean of Students, where staff members are available to assist students in interpreting the rules and regulations.

Incomplete Grade Policy: As per university policy, incomplete grades will be granted only for work unavoidably missed at the semester's end and only if 70% of the course work has been completed. An incomplete grade must be resolved within eight (8) weeks from the first day of the subsequent long semester. If the required work to complete the course and to remove the incomplete grade is not submitted by the specified deadline, the incomplete grade is changed automatically to a grade of **F**.

Disability Services: The goal of Disability Services is to provide students with disabilities educational opportunities equal to those of their non-disabled peers. Disability Services is located in room 1.610 in the Student Union. Office hours are Monday and Thursday, 8:30 a.m. to 6:30 p.m.; Tuesday and Wednesday, 8:30 a.m. to 7:30 p.m.; and Friday, 8:30 a.m. to 5:30 p.m.

The contact information for the Office of Disability Services is:

The University of Texas at Dallas, SU 22

PO Box 830688

Richardson, Texas 75083-0688

(972) 883-2098 (voice or TTY)

Essentially, the law requires that colleges and universities make those reasonable adjustments necessary to eliminate discrimination on the basis of disability. For example, it may be necessary to remove classroom prohibitions against tape recorders or animals (in the case of dog guides) for students who are blind. Occasionally an assignment requirement may be substituted (for example, a research paper versus an oral presentation for a student who is hearing impaired). Classes enrolled students with mobility impairments may have to be rescheduled in accessible facilities. The college or university may need to provide special services such as registration, note-taking, or mobility assistance.

It is the student's responsibility to notify his or her professors of the need for such an accommodation. Disability Services provides students with letters to present to faculty members to verify that the student has a disability and needs accommodations. Individuals requiring special accommodation should contact the professor after class or during office hours.

Religious Holy Days: The University of Texas at Dallas will excuse a student from class or other required activities for the travel to and observance of a religious holy day for a religion whose places of worship are exempt from property tax under Section 11.20, Tax Code, Texas Code Annotated.

The student is encouraged to notify the instructor or activity sponsor as soon as possible regarding the absence, preferably in advance of the assignment. The student, so excused, will be allowed to take the exam or complete the assignment within a reasonable time after the absence: a period equal to the length of the absence, up to a maximum of one week. A student who notifies the instructor and completes any missed exam or assignment may not be penalized for the absence. A student who fails to complete the exam or assignment within the prescribed period may receive a failing grade for that exam or assignment.

If a student or an instructor disagrees about the nature of the absence [i.e., for the purpose of observing a religious holy day] or if there is similar disagreement about whether the student has been given a reasonable time to complete any missed assignments or examinations, either the student or the instructor may request a ruling from the chief executive officer of the institution, or his or her designee. The chief executive officer or designee must take into account the legislative intent of TEC 51.911(b), and the student and instructor will abide by the decision of the chief executive officer or designee.

These descriptions and timelines are subject to change at the discretion of the instructor.