

## EE 2310 Homework Due Dates Spring Semester, 2012

Homework's are due in class on the date shown below. Normally, the TA will come to class to collect homework's. If you fail to turn in the homework in class, you can take it to the TA's office directly after class and turn it in. Be sure to mention that this is the current homework. The location of the TA's office is shown on the EE 2310 website. The deadline time for all homework's is class time on the date shown below.

<u>Assignment Number</u>	<u>Covers</u>	<u>Due</u>
1.	HW 1, Binary Numbers and Numeric Conversions	January 31
2.	HW 2, Digital Logic Circuits and Logic Simplification with Karnaugh Maps	February 9
3.	HW 3, Flip Flops and Timing Diagrams	February 14
4.	Test Review #1	February 14
5.	HW 4, Complex Flip-Flops and Sequential Logic	February 23
6.	HW 5, MIPS Instructions and Simple Programming	March 20
7.	HW 6, Branch Instructions: Loops	March 27
8.	Test Review #2	March 27
9.	Semester Design Project	April 12*
10.	HW 7, Complex SPIM Programs	April 19
11.	HW 8, Computer Architecture	April 26
12.	Bonus Questions for Test #3	April 26**
13.	Test Review #3	April 26

\* Note that semester project is due on April 12, but you must complete enough of this project to be well-prepared for test #2 on March 29!

\*\* Suggested completion date to be ready for test #3; this homework is not to be turned in.