

DANIE P. ALEX
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OBJECTIVE To obtain a position in Electrical Engineering field that provides opportunity and experience promoting organizational and professional skills.

EDUCATION

The University Of Texas at Dallas, Richardson, Texas
Master of Science in Electrical Engineering, Overall GPA: 4.0
Graduation Date: Fall 2009.

The University Of Texas at Dallas, Richardson, Texas
Bachelor of Science in Electrical Engineering, Overall GPA: 3.73
Graduated on December 8, 2007 with Magna Cum Laude Honors.

Richland College, Dallas, Texas
Associate of Arts and Sciences in Electrical Engineering, Overall GPA: 3.871

TKM Institute of Technology, Kollam, India
Associate of Science in Computer Science, Overall GPA: 3.82

RELEVANT COURSES Digital Circuits, Electronic Devices, Electronic Circuits, RF Circuit Design, Microwave Circuits and Systems, VLSI Design, Microwave Amplifier Design, Analog IC Design, Design and Test of RF components.

SKILLS

Tools Used: Keithley meters, Oscilloscopes, DC Power supply, Spectrum Analyzer, Xilinx Spartan-3 FPGA, Vector Network Analyzer (VNA).
Programming Languages: C, MIPS, Verilog.
Software: MatLab, Pspice, Hspice, Multisim, Xilinx ISE, LogicWorks, LabView, MS Office, AWR Microwave Office, VSS, Cadence IC design tools.

WORK EXPERIENCE

(Current) **Nortel Networks, Richardson, TX**
New Grad Leadership Program (NGLP)
Customer Service Account Manager, SharedTech: Act as a consultant to regional Acct teams and PMs for technical details and support. Identify process improvements that will eliminate waste.

(04/08 – 01/09) **NGLP Assignment 1→CSA, Verizon Operations:** Act as a consultant to regional Acct teams and PMs for technical details and support. Manage communications across to VzW Mtc Eng as well as local Ops, and Eng teams so that they have the information req'd for their decision making process. Identify process improvements that will eliminate waste.

(08/07-11/07) **UT Southwestern, Dallas, TX**
Student Intern, Department of Translational Research
Working on the development of a Holographic TV based on TI's DLP technology, solid state lasers and liquid crystal plates.

(11/03 – 4/05) **Mesquite Community Hospital, Mesquite, TX**
Tech Assistant, Imaging Services
Assisted imaging technicians (MRI, CAT Scan, Sonogram, X-Ray) in the imaging process.

ACADEMIC PROJECTS

PLL Design

Designed a Phase-Locked Loop circuit which can be used in a wide range of applications, including FM demodulation. The simulation of the circuit was done using Multisim and Pspice.

Phase Array Radar Receive Module

Designed a Ku-Band phase array radar receive module using microstrip transmission lines, pHEMT, and lumped elements. The design was done using AWR Microwave office.

Design of a Combinational Lock using 120 μ m Technology [VLSI]

Designed a FSM using verilog and used Cadence tools to create a library of standard cells and finally used the routing tool to layout the design.

Electric Fields inside FET Structures [Fields and Waves]

The purpose of the project was to find a numerical solution to Laplace's equation and analyze the electric fields inside FET structures. The analysis was done using MS Excel and Matlab.

RF Dual-Band Receiver Front-End Module

Designed a front-end module for a dual-band switched channel receiver working at 1.9 -2.1 GHz. The design was done using AWR Microwave office.

HONORS ACTIVITIES

RLC President's Honor List (Fall 2005), Golden Key International Honor Society
Institute of Electrical and Electronic Engineers (IEEE), Hobby Electronics.